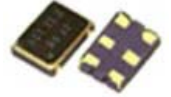


FTV75D Series

VCXO, 7.0 x 5.0mm, LVPECL, LVDS Output



REACH and RoHS compliant
 Factory programmable device
 Fast lead time
 Typical phase jitter 1.0ps

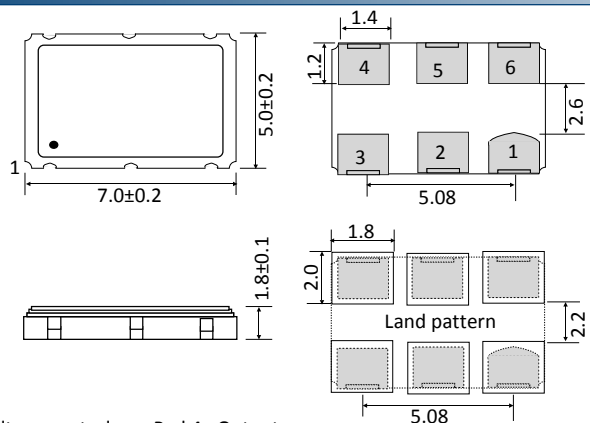


Parameters	Specification		Remarks
Frequency range	F_nom	10.0MHz ~ 1450.0MHz	
Supply voltage	Vcc	2.5V±5%, 3.3V±5%	
Frequency stability	F_stb	±25.0ppm, ±50.0ppm, ±100.0ppm	Includes initial tolerance, stability over temperature, load, Vcc
Aging	F_age	±2.0ppm max first year at 25°C	±10.0ppm max over 10 years
Operating temperature range (°C)	Topr	-10°C ~ +70°C, -40°C ~ +85°C	
Storage temperature (°C)	Tstg	-55°C ~ +125°C	
Output waveform		LVPECL, LVDS	
Output load		Differential	
Output voltage high	Voh	LVPECL: Vcc - 1.03 (min), Vcc - 0.6 (max)	LVDS: 1.4V typical, 1.6V max
Output voltage low	Vol	LVPECL: Vcc - 1.85 (min), Vcc - 1.6 (max)	LVDS: 1.1V typical, 0.9V min
Rise time	Tr	0.5nsec max, 0.2nsec typical	Measured over 20% to 80% of waveform
Fall time	Tf	0.5nsec max, 0.2nsec typical	
Duty cycle		45%/55%	Measured at 50% of waveform
Current consumption	Icc	Table 1	
Start-up time	T_str	10.0msec max	
Phase jitter (RMS)		1.0psec typical (12kHz to 20MHz)	<100fsec for 1.875kHz to 20MHz
Phase noise		Table 2	
Frequency pulling	F_pull	2.5V : ±90.0ppm min, 3.3V : ±100.0ppm min	Up to ±200.0ppm min is available
Control voltage	Vc	2.5V : 1.25V ± 1.05V, 3.3V : 1.65V ± 1.35V	
Slope / Linearity		Positive / ±10% max, ±5% typical	
Modulation bandwidth		10kHz min	Measured at -3dB
Input impedance		1MΩ typical	
Moisture sensitive level / ESD sensitive device	MSL	1 / Yes	
Tri-state		Enable high, No connection or 0.7% of Vcc min is applied to Tri-state pin to Enable output	
		0.3% of Vcc max to disable output (high impedance)	
Output enable time / Disable time		200nsec max / 50 nsec max	

Output	LVPECL		LVDS	
	2.5V	3.3V	2.5V	3.3V
100MHz	46mA	50mA	16mA	25mA
250MHz	48mA	55mA	18mA	30mA
500MHz	53mA	60mA	21mA	35mA
750MHz	56mA	64mA	22mA	39mA
1.0GHz	60mA	68mA	24mA	43mA
1.35GHz	65mA	72mA	26mA	47mA

Note: All values are typical over operating temperature range. Current without output is 16mA typical.

Dimensions(Unit:mm)



- Pad 1 : Voltage control
- Pad 2 : Tri-state
- Pad 3 : Ground
- Pad 4 : Output
- Pad 5 : Complementary Output
- Pad 6 : Suppl voltage

FTV75D Series

VCXO, 7.0 x 5.0mm, LVPECL, LVDS Output



Table 2. SSB Phase noise [dBc/Hz (typical)]

Offset	77.76MHz	122.88MHz	125MHz	156.25MHz	212.5MHz	491.52MHz	622.08MHz	1000MHz	1250MHz
10 Hz	-57	-68	-63	-55	-62	-61	-48	-52	-42
100 Hz	-94	-99	-94	-85	-93	-86	-85	-82	-81
1 kHz	-114	-113	-113	-109	-105	-100	-101	-93	-93
10 kHz	-123	-119	-118	-116	-113	-105	-102	-97	-96
100 kHz	-124	-120	-119	-118	-115	-105	-103	-97	-97
1 MHz	-144	-140	-137	-139	-135	-126	-124	-116	-119
10 MHz	-152	-148	-146	-146	-143	-137	-133	-127	-129
Phase jitter 12kHz~20MHz	0.9ps	0.8ps	1.1ps	0.9ps	1.0ps	1.1ps	1.2ps	1.5ps	1.1ps

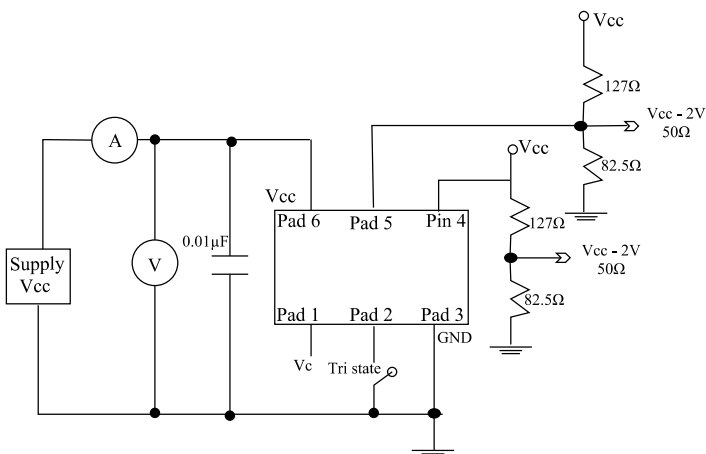
Part number generation

TV75	2600	B	B	I	E	L	K	P	H	N	L	-PF
ACT series Code	Frequency (MHz)	Frequency Stability all causes (±ppm)	Supply voltage (Vcc)	Operating temp. range (°C)	Linearity (± %)	Output wave	Minimum pulling (± ppm)	Polarity	Duty cycle (%/%)	Tristate	Tape & Reel	RoHS Code
TV75	26MHz = 2600 (Note 1)	25 = C 50 = B 100 = A	2.5 = C 3.3 = B	-10~+70 = C -20~+70 = B -40~+85 = I	10 = E	LVPECL = L LVDS = K	± 90 = K ± 100 = B ± 150 = C ± 200 = D	Positive = P	45/55 = H	Yes = P No = N	Loose = L 1000 = C	-PF

Note 1 : Frequency < 100MHz use first 4 digit of frequency in part numbering, Frequency > 100MHz use first 5 digit of frequency. Example: 26.00MHz = 2600, 8.00MHz = 0800, 14.7456MHz = 1474, 100MHz = 10000 .It is important to suffix the above part number with full frequency required to give a completed part number as illustrated below.

Full Example part number : TV75-2600BBIELKPHNL-PF [26MHz], TV75-1474BBIEKPHNL-PF [14.7456MHz]

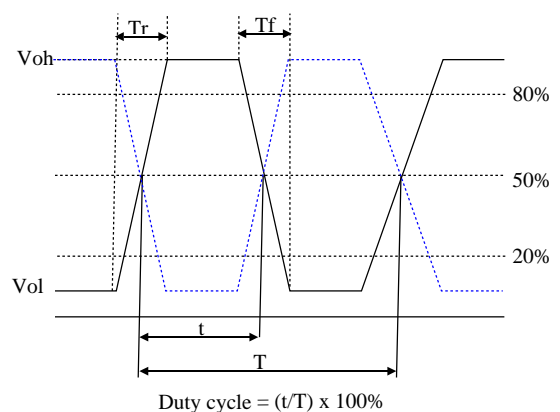
LVPECL test circuit



LVPECL Resistor values

Vcc	R1, R3	R2, R4
+3.3V	127Ω	82.5Ω
+2.5V	250Ω	62.5Ω

Waveform



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ISO9001 Registered

Specifications subject to change without notification

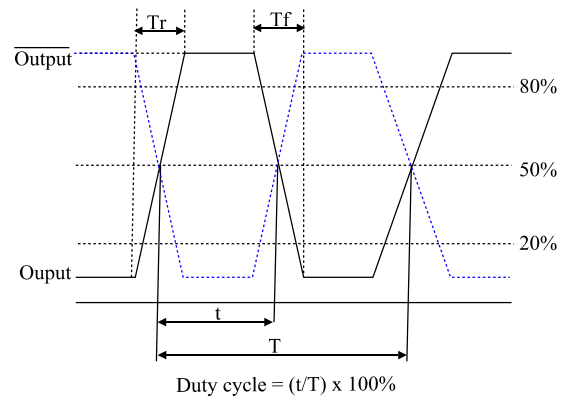
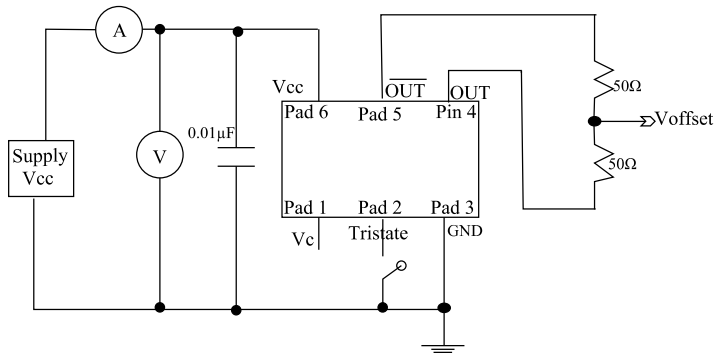
FTV75D Series

VCXO, 7.0 x 5.0mm, LVPECL, LVDS Output

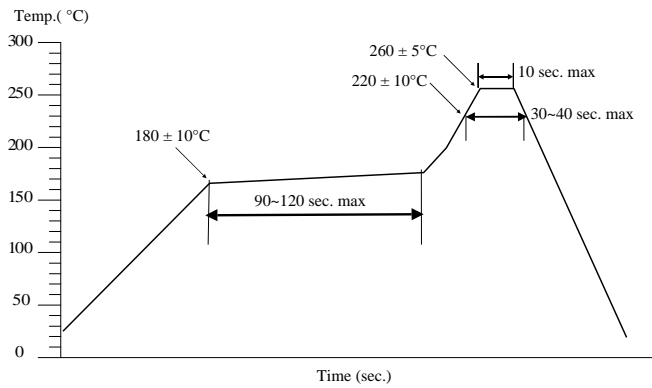


LVDS Test circuit

Test waveform



Solder reflow profile



Drawing control: (Internal use only)
 Commodity code: 844370 90 99
 Issue number : N1
 Date : 01/02/2017
 Internal reference : M6